

(Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 06269-020001	Application No. 09/662,194
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Kent D. Benson et al.	
		Filing Date September 14, 2000	Group Art Unit 2664

U.S. Patent Documents							
Examiner Initial	Desig. ID	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						
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	AE						
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Foreign Patent Documents or Published Foreign Patent Applications							
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation
							Yes No
	AH						
	AI						
	AJ						
	AK						
	AL						

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
TD	AM	McKeown et al. "A Quantitative Comparison of Scheduling Algorithms for Input-Queued Switches," Proceedings IEEE INFOCOM '96, pp. 296-299, San Francisco, 1996.
TD	AN	McKeown, N. "The iSLIP Scheduling Algorithm for Input-Queued Switches," IEEE/ACM Transactions on Networking, Vol. 7, No. 2, pp. 188-201, April 1999.
TD	AO	Charny, A. "Providing QoS Guarantees in Input Buffered Crossbar Switches with Speedup," Ph.D. dissertation, Massachusetts Institute of Technology, August, 1998.
TD	AP	Stephens, D. "Implementing Distributed Packet Fair Queueing in a Scalable Switch Architecture," M.S. Thesis, Carnegie-Melon University, 1998.
TD	AQ	Hedge et al. "Real-Time Adaptive Bandwidth Allocation for ATM Switches," Proceedings IEEE ICC '99, Vancouver, 1999.
TD	AR	Parekh, A. "A Generalized Processor Sharing Approach to Flow Control in Integrated Services Networks," Ph.D. dissertation, Massachusetts Institute of Technology, February 1992.
TD	AS	

Examiner Signature	Mel D	Date Considered	7/14/04
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			

Substitute Form PTO-1449 (Modified)		U.S. Department of Commerce Patent and Trademark Office		Attorney's Docket No. 06269-020001	Application No. 09/662,194 O I P E JC124
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U.S. Patent Documents							
Examiner Initial	Desig. ID	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date If Appropriate
TD	AA	5,463,620	10.31.1995	Sriram	370	60	—
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Foreign Patent Documents or Published Foreign Patent Applications							
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation
							Yes No
TD	AL	WO 97/14240 A	17.04.1997	WIPO	H04L	12/56	X
TD	AM	EP 0 680 180 A1	02.11.1995	EP	H04L	12/56	X
TD	AN	EP 0 859 492 A2	19.08.1998	EP	H04L	12/56	X
TD	AO	EP 0859 492 A3	11.08.1999	EP	H04L	12/56	X
TD	AP	EP 0 901 301 A2	10.03.1999	EP	H04Q	11/04	X
TD	AQ	EP 0 901 301 A3	12.07.2000	EP	H04Q	11/04	X

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
TD	AR	Hou, et al. "A Weighted Max-Min Fair Rate Allocation for Available Bit Rate Service," IEEE, 1997, pp 492-497.
TD	AS	Wallmeier, et al. "Traffic Control in ATM Switches with Large Buffers," ITC Specialists Seminar, NL, Leidschendam, KPN Research, Vol. Seminar 9, November 7, 1995, pp 45-60.
TD	AT	PCT International Search Report, Officer Meurisse, W., January 10, 2001

Examiner Signature	<i>Jue B</i>	Date Considered	<i>9/14/04</i>
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